



## SPECIFICATION

**MODULE NO.: WF43ETLAEDNCB#**

### General Specifications

Item	Dimension	Unit
Size	4.3	inch
Dot Matrix	480 RGB(H) x 272(V)	dots
Module dimension	105.5 x 67.2x 4.16 (Typ.)	mm
Active area	95.04(H) x 53.85(V)	mm
Dot pitch	0.198(H) x 0.198(V)	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	6 o'clock	
Gray Scale Inversion Direction	12 o'clock	
Aspect Ratio	16:9	
Backlight Type	Normally White	
With /Without TP	With CTP (FT5306)	
Surface	Glare	

\*Color tone slight changed by temperature and driving voltage.

# Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

# Electrical Characteristics

Ta = 25 °C

Item	Symbol	MIN	TYP	MAX	Unit	
Supply Voltage	VDD	3.0	3.3	3.6	V	
Supply Voltage For CTP	VDDT	2.8	—	3.3	V	
Input Signal Voltage	Low Level	VIL	GND	-	0.3x VDD	V
	High Level	VIH	0.7x VDD	-	VDD	V
Output Signal Voltage	Low Level	VIL	-	-	0.2x VDD	V
	High Level	VIH	0.8x VDD	-	VDD	V
(Panel + LSI) Power Consumption	Black Mode (60Hz)	-	74		nW	
	Standby	-	50	-	uW	

# Interface

## 8.1. LCM PIN Definition

No.	Symbol	Description
1	VBL-	Backlight LED Cathode
2	VBL+	Backlight LED Anode.
3	GND	System Ground
4	VDD	Power supply for logic operation
5~12	R0~R7	Data bus
13~20	G0~G7	Data bus
21~28	B0~B7	Data bus
29	GND	System Ground
30	DCLK	Pixel clock signal
31	DISP	Display on/off control
32	HSYNC	Horizontal Sync signal
33	VSYNC	Vrtical Sync signal
34	DE	Data Enable
35	NC	No connect
36	GND	System Ground
37	NC	No connect
38	NC	No connect
39	NC	No connect
40	NC	No connect

## 8.2. CTP PIN Definition

Pin	Symbol	Function
1	VSS	Ground for analog circuit
2	VDDT	Power Supply : +3.0V
3	SCL	I2C clock input
4	NC	No connect
5	SDA	I2C data input and output
6	NC	No connect
7	/RST	External Reset, Low is active
8	/WAKE	External interrupt from the host
9	/INT	External interrupt to the host
10	VSS	Ground for analog circuit

# Contour Drawing

