



SPECIFICATION

MODULE NO.: WF43HSIAEDNGB#

General Specifications

| Item | Dimension | Unit |
|--------------------------------|-----------------------------------|------|
| Size | 4.3 | inch |
| Dot Matrix | 480 x RGBx272(TFT) | dots |
| Module dimension | 105.5 x 67.2 x 4.6 | mm |
| Active area | 95.04 x 53.856 | mm |
| Dot pitch | 0.066 x 0.198 | mm |
| LCD type | TFT, Normally White, Transmissive | |
| View Direction | 12 o'clock | |
| Gray Scale Inversion Direction | 6 o'clock | |
| Aspect Ratio | 16:9 | |
| Backlight Type | LED, Normally White | |
| CTP FW Version | 8 | |
| With /Without TP | With CTP | |
| Surface | Glare | |

*Color tone slight changed by temperature and driving voltage.

Absolute Maximum Ratings

| Item | Symbol | Min | Typ | Max | Unit |
|-----------------------|--------|-----|-----|-----|------|
| Operating Temperature | TOP | -20 | — | +70 | °C |
| Storage Temperature | TST | -30 | — | +80 | °C |

Electrical Characteristics

| Item | Symbol | Condition | Min | Typ | Max | Unit |
|--------------------------------|--------|-----------|-----|------|-------|------|
| Power Supply For Voltage | VDD | - | 3.1 | 3.3 | 3.6 | V |
| Power Supply For Current | IDD | - | - | 22 | 33 | mA |
| Power Consumption | - | - | - | 72.6 | 118.5 | mW |
| Supply Voltage For Touch Logic | VDDT | — | 2.8 | — | 3.3 | V |

Interface

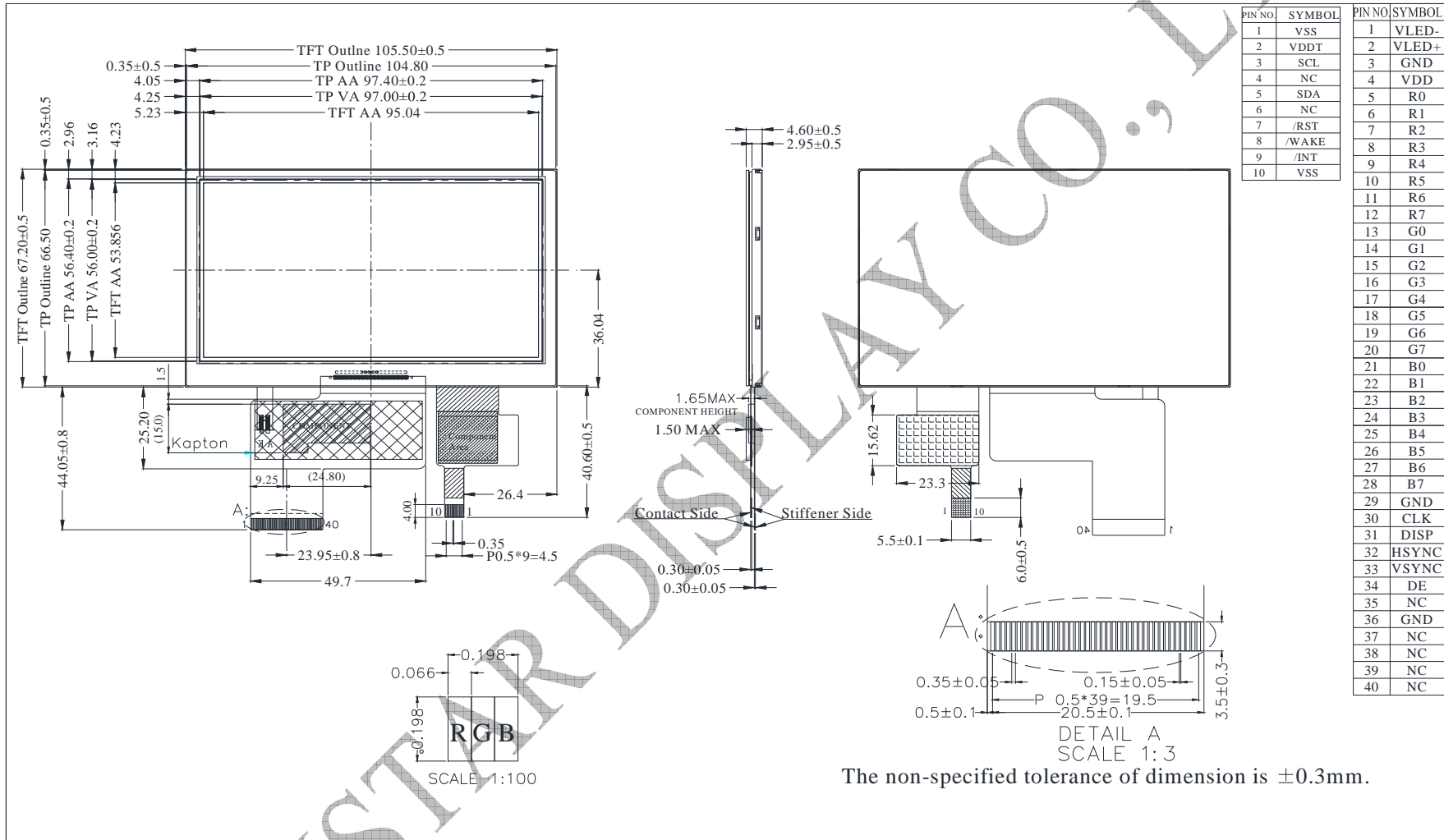
9.1. LCM PIN Definition

| Pin | Symbol | Function |
|-----|--------|---|
| 1 | VLED- | Power for LED backlight cathode |
| 2 | VLED+ | Power for LED backlight anode |
| 3 | GND | Power ground |
| 4 | VDD | Power voltage |
| 5 | R0 | Red data (LSB) |
| 6 | R1 | Red data |
| 7 | R2 | Red data |
| 8 | R3 | Red data |
| 9 | R4 | Red data |
| 10 | R5 | Red data |
| 11 | R6 | Red data |
| 12 | R7 | Red data (MSB) |
| 13 | G0 | Green data (LSB) |
| 14 | G1 | Green data |
| 15 | G2 | Green data |
| 16 | G3 | Green data |
| 17 | G4 | Green data |
| 18 | G5 | Green data |
| 19 | G6 | Green data |
| 20 | G7 | Green data (MSB) |
| 21 | B0 | Blue data (LSB) |
| 22 | B1 | Blue data |
| 23 | B2 | Blue data |
| 24 | B3 | Blue data |
| 25 | B4 | Blue data |
| 26 | B5 | Blue data |
| 27 | B6 | Blue data |
| 28 | B7 | Blue data (MSB) |
| 29 | GND | Power ground |
| 30 | CLK | Pixel clock |
| 31 | DISP | Display on/off |
| 32 | HSYNC | Horizontal sync signal; negative polarity |
| 33 | VSYNC | Vertical sync signal; negative polarity |
| 34 | DE | Data Enable |
| 35 | NC | No connection |
| 36 | GND | Power ground |
| 37 | NC | No connect |
| 38 | NC | No connect |
| 39 | NC | No connect |
| 40 | NC | No connect |

9.2. CTP PIN Definition

| Pin | Symbol | Function |
|-----|--------|----------------------------------|
| 1 | VSS | Ground for analog circuit |
| 2 | VDDT | Power Supply : +3.3V |
| 3 | SCL | I2C clock input |
| 4 | NC | No connect |
| 5 | SDA | I2C data input and output |
| 6 | NC | No connect |
| 7 | /RST | External Reset, Low is active |
| 8 | /WAKE | External interrupt from the host |
| 9 | /INT | External interrupt to the host |
| 10 | VSS | Ground for analog circuit |

Contour Drawing



| PIN NO. | SYMBOL | PIN NO. | SYMBOL |
|---------|--------|---------|--------|
| 1 | VSS | 1 | VLED- |
| 2 | VDDT | 2 | VLED+ |
| 3 | SCL | 3 | GND |
| 4 | NC | 4 | VDD |
| 5 | SDA | 5 | R0 |
| 6 | NC | 6 | R1 |
| 7 | /RST | 7 | R2 |
| 8 | /WAKE | 8 | R3 |
| 9 | /INT | 9 | R4 |
| 10 | VSS | 10 | R5 |
| | | 11 | R6 |
| | | 12 | R7 |
| | | 13 | G0 |
| | | 14 | G1 |
| | | 15 | G2 |
| | | 16 | G3 |
| | | 17 | G4 |
| | | 18 | G5 |
| | | 19 | G6 |
| | | 20 | G7 |
| | | 21 | B0 |
| | | 22 | B1 |
| | | 23 | B2 |
| | | 24 | B3 |
| | | 25 | B4 |
| | | 26 | B5 |
| | | 27 | B6 |
| | | 28 | B7 |
| | | 29 | GND |
| | | 30 | CLK |
| | | 31 | DISP |
| | | 32 | HSYNC |
| | | 33 | VSYNC |
| | | 34 | DE |
| | | 35 | NC |
| | | 36 | GND |
| | | 37 | NC |
| | | 38 | NC |
| | | 39 | NC |
| | | 40 | NC |

The non-specified tolerance of dimension is ±0.3mm.