



WINSTAR Display Co.,Ltd.  
華凌光電股份有限公司



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### SPECIFICATION

**MODULE NO.: WF62CTIAGDNNA#**

### General Specifications

Item	Dimension	Unit
Size	6.2	inch
Dot Matrix	800 x 480	dots
Module dimension	155.2 x 89.2 x 3.5	mm
Active area	137.52 x 77.232	mm
Dot pitch	0.1719 x 0.1609	mm
LCD type	Normally White	
Gray Scale Inversion Direction	6 o'clock	
View Direction	12 o'clock	
Interface	RGB-24bit	
Aspect Ratio	16:9	
Backlight Type	LED, Normally White	
Touch Panel	Without Touch Panel	
Surface	Anti-Glare	

\*Color tone slight changed by temperature and driving voltage.

# Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

# Electrical Characteristics

## 1. TFT LCD Module

Item	Symbol	Min	Typ	Max	Unit
Digital supply voltage	DVDD	3	3.3	3.6	V
Analog supply voltage	AVDD	9.0	9.2	9.1	V
Gate On voltage	VDDG	17	18	19	V
Gate Off voltage	VEEG	-6.6	-6	-5.4	V
Common voltage	VCOM	3.8	4	4.2	V
Gamma voltage	VR1	-	9.01	-	V
	VR2	-	7.22	-	V
	VR3	-	6.88	-	V
	VR4	-	6.57	-	V
	VR5	-	5.525	-	V
	VR6	-	3.925	-	V
	VR7	-	2.854	-	V
	VR8	-	2.484	-	V
	VR9	-	2.04	-	V
	VR10	-	0.21	-	V
Logic input voltage	VIH	0.7DVDD	-	DVDD	V
	VIL	GND	-	0.3DVDD	V

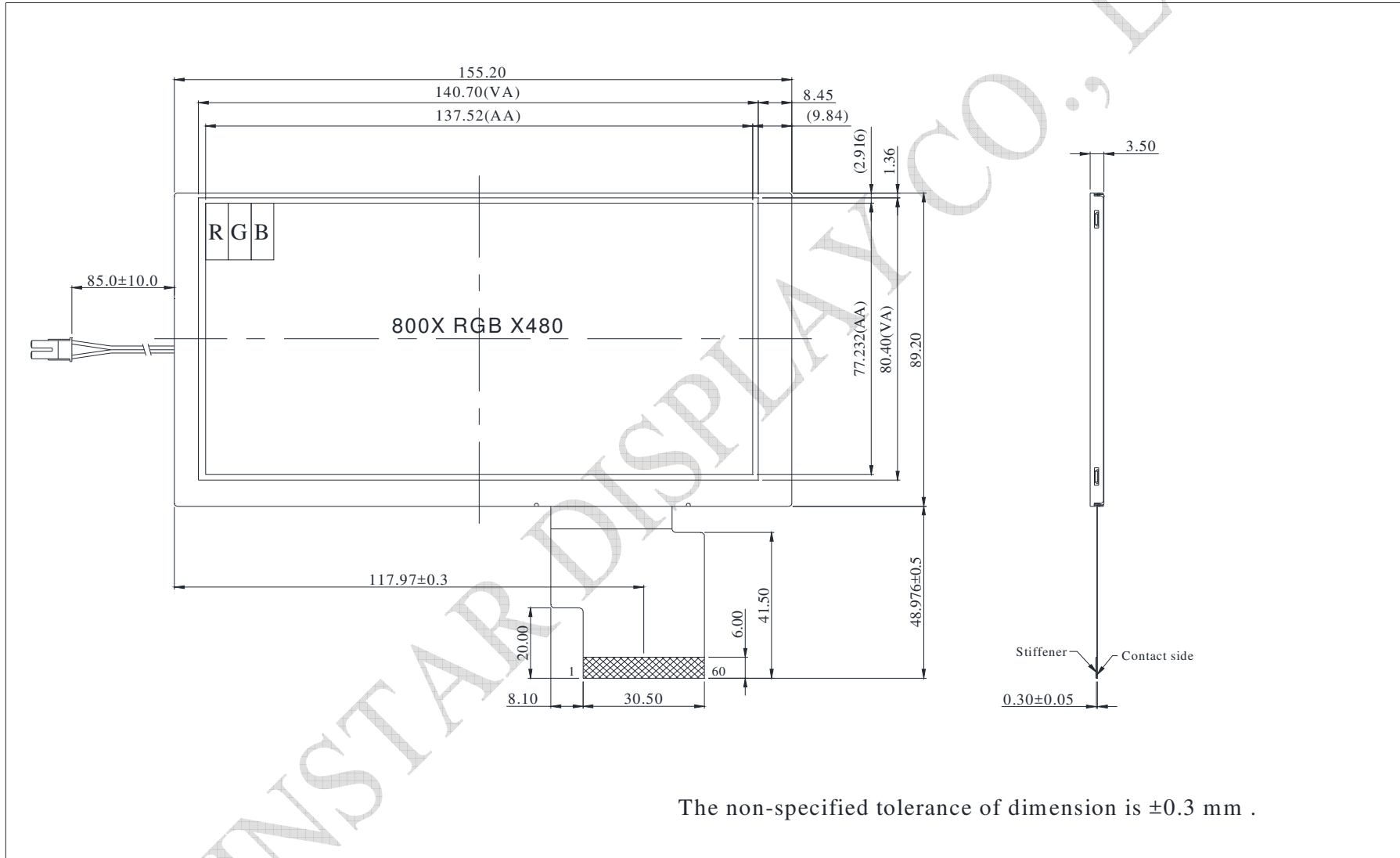
# Interface

## 1. LCM PIN Definition

No.	Symbol	Description
1	AGND	System Ground
2	AVDD	Analog power
3	DVDD	Power supply for logic operation
4~11	R0~R7	Data bus
12~19	G0~G7	
20~27	B0~B7	
28	DOTCLK	Pixel clock signal
29	DE	Data Enable
30	HSD	Horizontal Sync signal
31	VSD	Vertical Sync signal
32	MODE	DE/SYNC mode select. Normally pull high H: DE mode. L: HSD/VSD mode
33	RSTB	Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high. (R=47KΩ, C=1μ)
34	STBYB	Standby mode, normally pull high STBYB="1", normal operation STBYB="0", timing control, source driver will turn off, all output are high-Z
35	SHLR	Left or Right Display Control
36	DVDD	Digital Power
37	UPDN	Up / Down Display Control
38	GND	Digital Ground
39	AGND	Analog Ground
40	AVDD	Analog Power
41	VCOM	Common Voltage

42	DITH	Dithering setting DITH="H" 6bit resolution(last 2 bit of input data truncated) (default setting) DITH="L" 8bit resolution
43,44	NC	Not connect
45	V10	Gamma correction voltage reference
46	V9	Gamma correction voltage reference
47	V8	Gamma correction voltage reference
48	V7	Gamma correction voltage reference
49	V6	Gamma correction voltage reference
50	V5	Gamma correction voltage reference
51	V4	Gamma correction voltage reference
52	V3	Gamma correction voltage reference
53	V2	Gamma correction voltage reference
54	V1	Gamma correction voltage reference
55	NC	Not connect
56	VDDG	Positive Power for TFT
57	DVDD	Digital Power
58	VEEG	Negative Power for TFT
59	GND	Digital Ground
60	NC	Not connect

# Contour Drawing

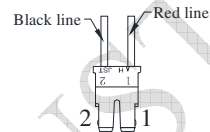
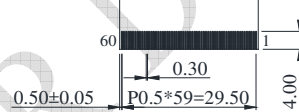


PIN NO.	SYMBOL
1	AGND
2	AVDD
3	DVDD
4-11	R0-R7
12-19	G0-G7
20-27	B0-B7
28	DOTCLK
29	DE
30	HSD
31	VSD
32	MODE
33	RSTB
34	STBYB
35	SHLR
36	DVDD
37	UPDN
38	GND
39	AGND
40	AVDD
41	VCOM
42	DITH
43-44	NC
45	V10
46	V9
47	V8
48	V7
49	V6
50	V5
51	V4
52	V3
53	V2
54	V1
55	NC
56	VDDG
57	DVDD
58	VEEG
59	GND
60	NC



CON1

1	LEDA
2	LEDK



BHSR-02VS-1

The non-specified tolerance of dimension is  $\pm 0.3$  mm .